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Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	481	713/172.ccls.	US-PGPUB; USPAT	OR	OFF	2007/09/11 12:19
L2	1963	713/193.ccls.	US-PGPUB; USPAT	OR	OFF	2007/09/11 12:19
L3	316	705/66,71.ccls.	US-PGPUB; USPAT	OR	OFF	2007/09/11 12:20
L4	44087	(ic or smart or chip or monolithic) adj card\$1	US-PGPUB; USPAT	OR	OFF	2007/09/11 12:20
L5	2689	I1 or I2 or I3	US-PGPUB; USPAT	OR	OFF	2007/09/11 12:20
L6	45859	I4 or I5	US-PGPUB; USPAT	OR	OFF	2007/09/11 12:23
L7	0	I6 and (select\$4 or determin\$4 or read) near3 (sensitive or secure) near3 (data or information) near3 (storage or memory or store) near4 transmi\$4 near5 bus	US-PGPUB; USPAT	OR	OFF	2007/09/11 12:25
L8	2	I6 and (select\$4 or determin\$4 or read) with (sensitive or secure)with (data or information) with (storage or memory or store) with transmi\$4 near5 bus	US-PGPUB; USPAT	OR	OFF	2007/09/11 12:26
L9	2	I6 and (select\$4 or determin\$4 or read) with (sensitive or secure) with (data or information) with (storage or memory or store) with transmi\$4 near5 bus	US-PGPUB; USPAT	OR	OFF	2007/09/11 12:27
L10	3	I6 and (select\$4 or determin\$4 or read or verif\$4) with (sensitive or secure) with (data or information) with transmi\$4 with bus	US-PGPUB; USPAT	OR	OFF	2007/09/11 12:29
L11	13	(select\$4 or determin\$4 or read or verif\$4) with (sensitive or secure) with (data or information) with transmi\$4 with bus	US-PGPUB; USPAT	OR	OFF	2007/09/11 12:33
L12	123	I3 and (verify\$4 or validat\$4 or determin\$4 or check\$4 or read) with (code or operation adj code or instruction adj code or instruction)	US-PGPUB; USPAT	OR	OFF	2007/09/11 12:35
L13	0	I3 and (verify\$4 or validat\$4 or determin\$4 or check\$4 or read) with (code or operation adj code or instruction adj code or instruction) with transmi\$4 near3 bus with table	US-PGPUB; USPAT	OR	OFF	2007/09/11 12:36

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L14	0	I3 and (verify\$4 or validat\$4 or determin\$4 or check\$4 or read) with (code or operation adj code or instruction adj code or instruction) with ("same" or identical) with bit\$1	US-PGPUB; USPAT	OR	OFF	2007/09/11 12:38
L15	2	I3 and (verify\$4 or validat\$4 or determin\$4 or check\$4 or read) with (code or operation adj code or instruction adj code or instruction) with ("same" or equal or identical) with (value or bit\$1)	US-PGPUB; USPAT	OR	OFF	2007/09/11 12:39
L16	0	I3 and (verify\$4 or validat\$4 or determin\$4 or check\$4 or read) with (code or operation adj code or instruction adj code or instruction) with (equal or identical) with condition	US-PGPUB; USPAT	OR	OFF	2007/09/11 12:40
L17	6	I3 and (verify\$4 or validat\$4 or determin\$4 or check\$4 or read) with (code or operation adj code or instruction adj code or instruction) with (equal or identical)	US-PGPUB; USPAT	OR	OFF	2007/09/11 12:41
L18	0	I3 and (verify\$4 or validat\$4 or determin\$4 or check\$4 or read) with (code or operation adj code or instruction adj code or instruction) with table with (valid or legit\$6)	US-PGPUB; USPAT	OR	OFF	2007/09/11 12:41
L19	81	I6 and (verify\$4 or validat\$4 or determin\$4 or check\$4 or read) with (code or operation adj code or instruction adj code or instruction) with table with (valid or legit\$6)	US-PGPUB; USPAT	OR	OFF	2007/09/11 12:42
L20	1	I6 and (verify\$4 or validat\$4 or determin\$4 or check\$4 or read) with (code or operation adj code or instruction adj code or instruction) with table with (valid or legit\$6) with ("same" or equal or identical)	US-PGPUB; USPAT	OR	OFF	2007/09/11 12:42
L21	81	I6 and (verify\$4 or validat\$4 or determin\$4 or check\$4 or read) with (code or operation adj code or instruction adj code or instruction) with table with (valid or legit\$6)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/11 12:50
L22	3	I6 and (verify\$4 or validat\$4 or determin\$4 or check\$4 or read) with (code or operation adj code or instruction adj code or instruction) with table with (valid or legit\$6) near10 (value or bit\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/11 12:56

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L23	7	I21 and @ad<"20010201"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/11 12:54
L24	0	I6 and (verify\$4 or validat\$4 or determin\$4 or check\$4 or read) with (code or operation adj code or instruction adj code or instruction) with table with (valid or legit\$6) near10 ("same" or equal or equival\$4 or identical) near10 (binary or bit\$1 or vlaue)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/11 12:57
L25	4	((smart or ic or integrated adj circuit or chip or monolithic) near3 card\$1). clm. and(verify\$4 or validat\$4 or determin\$4 or check\$4 or read).clm. and (code or operation adj code or instruction adj code or instruction). clm. and table and (valid or legit\$6). clm. and ("same" or equal or equival\$4 or identical).clm. and (binary or bit\$1 or vlaue).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/11 12:59
L26	14	((MICHEL) near2 (HAZARD)).INV.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/09/11 13:01

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L2	0	l1 and integrity near3 check\$4 with logic near3 operator near4 (entry or end\$3) near4 bus with compar\$4	US-PGPUB; USPAT; USOCR	OR	OFF	2007/09/11 14:09
L3	0	l1 and (integrity near3 check\$4 with logic near3 operator).clm. and (entry or end\$3).clm. and bus.clm. and compar\$4.clm.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/09/11 14:10
S2	14247	(ic or smart or chip or monolithic) adj card\$1	USPAT	OR	OFF	2007/09/11 12:20



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David C. Smith, Barry S. Wagner

January 1982 **Proceedings of the 19th conference on Design automation DAC '82**

Publisher: IEEE Press

Full text available: [pdf\(1.02 MB\)](#)

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The philosophy, structure, and design of a data-management system that is applicable to a wide range of organizational environments - from production groups to research and development groups - are introduced and discussed. The system is in operation in two locations and has been used to design numerous LSI/VLSI chips. This low cost system is transportable, extendable, and suitable for use by designers having a diversity of CAD-related skill levels. User reaction to the system and future pl ...

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David C. Smith, Barry S. Wagner

January 1982 **Proceedings of the 19th conference on Design automation DAC '82**

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IEEE CNF IEEE Conference Proceeding

IET CNF IET Conference Proceeding

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- ☐ **1. Static property checking using ATPG vs. BDD techniques**
 Chung-Yang Huang; Bwolen Yang; Huan-Chih Tsai; Kwang-Ting Cheng;
Test Conference, 2000. Proceedings. International
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- ☐ **2. Design and DCVS implementation of a self-checking bus-monitor unit for fault-tolerant system configurations**
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- ☐ **3. Dependable systems using 'VIPER'**
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- ☐ **4. Computer-based design for tomorrow's super chip**
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- ☐ **5. Performance analysis and simulation of the SOME-Bus architecture using passing**
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- ☐ **6. A flexible compatible PCI interface for nuclear experiments**
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- ☐ **11. On-chip integrated silicon bulk-micromachined soil moisture sensor with bus interface**
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- ☐ **12. Fieldbus interface control IC**
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- ☐ **13. Optimizing the package design with electrical modeling and simulation**
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- ☐ **14. High-level crosstalk defect simulation for system-on-chip interconnects**
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- ☐ **15. A 10000 frames/s CMOS digital pixel sensor**
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[Solid-State Circuits Conference, 2000. Digest of Technical Papers. ISSCC, 20](#)
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- ☐ **17. Coplanar bus model validation using test board and high speed CMOS di**
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[International](#)
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- ☐ **18. On the bus arbitration for MPEG 2 video decoder**
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- ☐ **19. Detection of control flow errors using signature and checking instruction**
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- ☐ **20. Two architectures for implementing RBF neural networks**
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Volume 3, 27 June-2 July 1994 Page(s):1963 - 1968 vol.3
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- ☐ **21. Multivariate process modeling: the 'preprocessing' challenge**
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- ☐ **22. Current ratios: a self-scaling technique for production IDDQ testing**
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- ☐ **23. A circuit technology for high-speed battery-operated 16-Mb CMOS DRAM**
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Sawada, K.; Ohnishi, T.; Fukumoto, M.; Fijita, T.; Inoue, M.;
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- ☐ **24. Implementation of a DSP based active power filter for electric power dist
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- ☐ **25. An analysis and comparison of different types of electronic payment sys**
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David C. Smith, Barry S. Wagner

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